

	L #	Hit	S arch Text	DB
1	L1	204	439/69.ccls.	USP AT; US-P GPU B
2	L2	118	1 and package\$1	USP AT; US-P GPU B

	L #	Hits	S ar h T xt	DBs
1	L1	802	multi\$1chip adj package\$1	USP AT; US-P GPU B
2	L2	86	1 and 257/686.ccls.	USP AT; US-P GPU B
3	L3	6	1 and 361/790.ccls.	USP AT; US-P GPU B
4	L4	84	2 not 3	USP AT; US-P GPU B